



B) JFW

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Batch/Confirmation No.: 9701

Chikao IKENAGA et al.

Allowed: February 13, 2006

Group Art Unit: 2822

Application No.: 10/781,947

Examiner: S. BARNES

Filed: February 20, 2004

Docket No.: 123784

For: SEMICONDUCTOR DEVICE FABRICATING APPARATUS AND  
SEMICONDUCTOR DEVICE FABRICATING METHOD

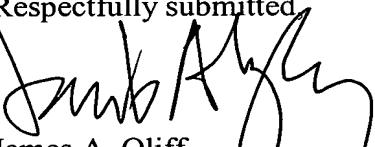
LETTER TO THE OFFICIAL DRAFTSPERSON

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please substitute the attached 7 sheets of formal drawings depicting Figures 1-8 for the corresponding drawings filed with the application. The attached drawings have been corrected pursuant to the Examiner's Amendment set forth in the February 13, 2006 Notice of Allowance.

Respectfully submitted,

  
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Jacob A. Doughty  
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JAO:JAD/tje

Date: March 13, 2006

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